

Figure 1

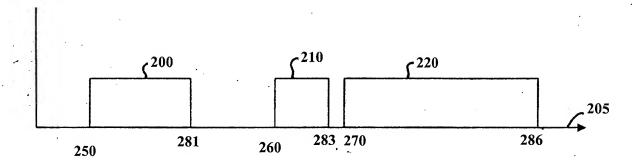


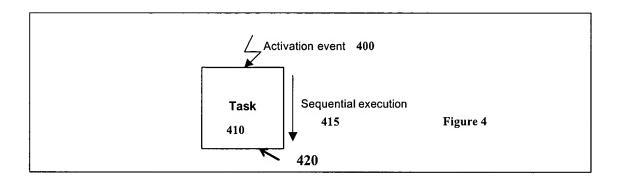
Figure 2

310	DISPATCHER ROUND OF 30 MS	INTERUPT ENABLE TIME	DEADLINE (WORST	
	320	330	CASE)	
		(MINIMUM TIME BETWEEN	340	
		SUCCESSIVE INTERRUPTS)		
Process	Time			
Task 1 322	0		20	
Task 2 324	10		30	
Task 3 326	25		60	
Idle				
ISR 1 332		4		
ISR 1 332		8		
		***		
ISR 1 332		28		
ISR 2 334		14		

Fig. 3A

310	DISPATCHER ROUND OF 30 MS	INTERUPT ENABLE TIME	DEADLINE (WORST CASE)
	320	330	
			340
		(MINIMUM	
		TIME	
		BETWEEN	
		SUCCESSIVE	
		INTERRUPTS)	
Process	Time		
Task 1 322	1		8
Task 2 324	10		20
Task 3 326	15		30
Idle			
ISR 1 332		4	
ISR 1 332		8	
		***	
ISR 1 332		28	
ISR 2 334			

Fig. 3B



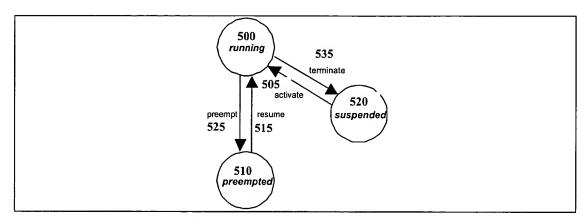


Figure 5

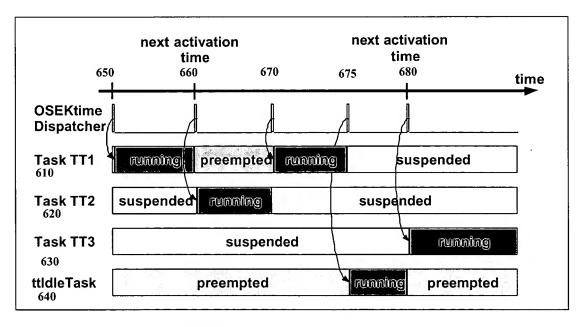


Figure 6

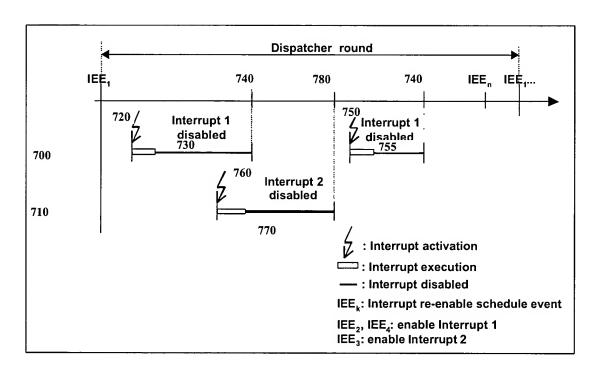


Figure 7

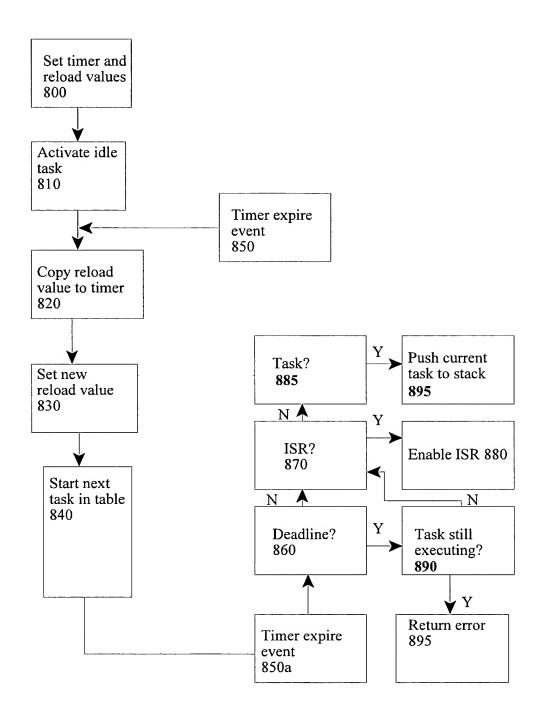


Fig. 8

60 900	Task 1 910	
50	Idle	
40	task 2	
30		
20		
10		
0		

Fig. 9